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AMENDMENTS TO THE SPECIFICATION

Please amend paragraph [0018], which begins on page 6 of the specification as shown in the below marked up paragraph.

[0001] Although the scope of the present invention is not limited in this respect, fractional N synthesizer 310 may generate output signal 345 with the desired frequency. In this embodiment, fractional N divider 320 may divide a frequency provided by VCO 340 by N, wherein N may be a rational fraction of integers M and L, i.e., $N=M/L$, wherein $0<M<L$, such that $0<N<1$. PLL 330 may set the frequency of output signal 345 by controlling the voltage of VCO 340. PLL 330 may include a phase detector (not shown) to lock the phase of the desired frequency. PLL 330 may vary the voltage of VCO 340 according to the detected phase. Accordingly, VCO 340 may provide output signal 345 at the desired frequency, which may be set by PLL [[340]] 330.